



ICP2-GANG(G3)

4 channel in-circuit programmer

Product Overview:

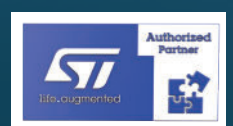
The ICP2-GANG(G3) is a robust four-channel programmer designed for a heavy mass production environment. Highly reliable and in production since 2006, this unit can flash four targets in a typical application or expand up to 64 targets in a daisy chain configuration. The programmer supports popular programming interfaces, including ICSP, SWD, JTAG, UPDI, I2C, SPI, TPI, PDI, UART, Microwire, SWIM, SWI, BDM, C2, SBW, etc., and works with any silicon chip. It is used with Microchip, ST, Renesas, NXP, Nordic, Cypress, Texas Instruments, Silicon Labs, GigaDevice, and other microcontroller vendors.

The ICP2-GANG(G3) scales up your production at high speed. Our Gap Eliminator™ technique reduces image size, while true parallel programming processes ensure that the targets are flashed at one time. Our speed optimization utility, which is built into the GUI interface, automatically configures the programmers' settings for optimal speed performance. As a result, you reduce time during the manufacturing process, generating operational cost savings.

Our technical support team is available around the clock to ensure you have immediate solutions for any operational or maintenance issues that arise.

- Production flash programmer
- Highly reliable since 2006
- Fast programming
- DLL / Command Line / Advanced Command Line Support (optional)
- Secure programming - innovative, multi-layer IP protection (optional)
- 24x7 Support

Our Partners



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High-Speed, Scalable Programming Device

- ICSP, SWD, JTAG, UPDI, I2C, SPI, TPI, PDI, UART, Microwire, SWIM, SWI, BDM, C2, SBW, etc. programming interfaces
- Ultra fast programming of up to 64 channels simultaneously
- USB and RS-232 interfaces
- On-board 32MByte flash memory per channel for non-volatile storage of HEX, configuration and serialization files (6 environments)
- Multiple serialization schemes: sequential, random, pseudo-random and user file, 1 to 8 bytes
- Tests Vdd and Vpp for overload with indication by software
- Dimensions: 195 x 110 x 68mm

Programmable Hardware

- Wired remote programming via 3 lines: GO/PASS/FAIL
- Programmable Vdd (1.8 to 5.5V) and Vpp (1.8 to 13.5V)
- Programmable delay between Vdd and Vpp (0.1 to 250ms)
- Programmable clock/data speed (500KHz to 10MHz)
- Programmable Vdd source: ICP2-GANG(G3) or target
- Vdd current limit: 250mA per channel
- Prevents damage to connected microcontroller in case of power ON and power OFF

Easy-to-Use Software

- DLL / Command Line / Advanced Command Line for automatic programming (optional)
- Secure programming feature including hex file encryption, counter and secure buffer (optional)
- 6 environments
- Daisy chain setup possibility to increase capacity to 64 channels

Host System Requirements

- Windows®- 7/8/10/11
- Linux with Wine
- USB or RS-232 port



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